

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,911,371 B2
APPLICATION NO. : 09/879335
DATED : June 28, 2005
INVENTOR(S) : Vishnu K. Agarwal et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page Item (75) Inventors

Add: Kunal R. Parekh, Boise, ID (US); and
Randhir P.S. Thakur, San Jose, CA (India)

Title page Item (56) References cited add the following patents to the list of U.S. Patent Documents:

5,796,166	8/1998	Agnello.....	257/751
5,699,291	12/1997	Tsunemine.....	438/240
5,608,249	3/1997	Gonzalez.....	257/306
6,165,833	12/2000	Parekh et al.....	438/240

Related Patent Data, column 1, line 12

Replace "a continuation application of U.S. Ser. No. 08/944,054"
With --a continuation application of U.S. Ser. No. 08/994,054--

Column 1, line 35

Replace "A shift threshold voltage (V_i)"
With --A shift in threshold voltage (V_i)--

Column 4, line 13

Replace "being NO—."
With --being NO--

Column 4, line 19

Replace "particularly NO—. Diffusion of impurities and other"
With --particularly NO-. Diffusion of impurities and other--

Column 8, line 4

Replace "One potential advantage of wafer construction and wafer"
With --One potential advantage of wafer 50 construction and wafer--

Column 7, line 63

Replace "FIG. 13 shows wafer construction after"
With --FIG. 13 shows wafer construction 50 after--

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Column 7, line 59

Replace "FIG. 12 shows a wafer construction including wafer"
With --FIG. 12 shows a wafer construction 50 including wafer--

Signed and Sealed this

Fifth Day of February, 2008

A handwritten signature in black ink that reads "Jon W. Dudas". The signature is written in a cursive style with a large, looped initial "J".

JON W. DUDAS
Director of the United States Patent and Trademark Office